

In the Claims

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25. (Previously Presented) A processor-implemented method for printing a test pattern, comprising:

determining a size of a print medium upon which the test pattern is to be printed;

configuring the test pattern to include as many color ramps as will fit per row based on the size of the print medium, wherein height of the test pattern is increased in response to availability of color ramps beyond which will fit in a row on the print medium, and wherein the color ramps are arrayed horizontally along a width of the print medium when the size of the print medium allows, and are arrayed vertically when the size of the print medium requires; and

printing the test pattern on the print medium.

1 26. (Previously Presented) The processor-implemented method of claim
2 25, wherein configuring the test pattern comprises:

3 configuring the test pattern to include a plurality of color ramps, wherein
4 each color ramp includes a number of color patches, wherein the number of color
5 patches is based on the size of the print medium.

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7 27. (Previously Presented) The processor-implemented method of claim
8 25, wherein configuring the test pattern comprises:

9 configuring the test pattern to include a second row only when space does
10 not exist on a first row to add an additional color ramp.

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13 28. (Previously Presented) The processor-implemented method of claim
14 25, wherein configuring the test pattern comprises:

15 configuring different test patterns for differently sized print media to
16 include different numbers of color ramps per row and different numbers of color
17 patches per color.
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1 29. (Previously Presented) The processor-implemented method of claim
2 25, wherein configuring the test pattern comprises:

3 configuring the color ramps to be printed based on adjustment of variables,
4 the variables comprising:

5 a number of color ramps within the test pattern;

6 a number of color patches within each color ramp;

7 a height of the color patches; and

8 a width of the color patches.
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11 30. (Previously Presented) The processor-implemented method of claim
12 25, wherein configuring the test pattern comprises:

13 moving the color ramps relative to each other to maximize width of the test
14 pattern and minimize height of the test pattern.
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17 31. (Previously Presented) The processor-implemented method of claim
18 25, wherein configuring the test pattern comprises:

19 adjusting a relative position of the color ramps, between locating the color
20 ramps on a same row and locating the color ramps on two different rows, based
21 upon size of the color ramps and space available in the same row.
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1 32. (Previously Presented) The processor-implemented method of claim
2 25, wherein as many color ramps as will fit per row is based on a size of the color
3 ramps and a width of the print medium.

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1 33. (Previously Presented) A printing apparatus configured for printing a
2 test pattern, comprising:

3 a print engine configured to print the test pattern according to instructions
4 for:

5 determining a width of a print medium;

6 configuring the test pattern to include as many color ramps as will
7 fit per row of the test pattern, based on the width of the print medium and size of
8 the color ramps, wherein the configuring is performed using parameters,
9 comprising:
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11 a number of color ramps within the test pattern;

12 a number of color patches within each color ramp;

13 a height of the color patches; and

14 a width of the color patches; and

15 adjusting the parameters, wherein the adjustment arrays the color
16 ramps horizontally along the width of the print medium when the size of the print
17 medium allows, and wherein the color ramps are arrayed vertically when the size
18 of the print medium requires; and
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20 printing the test pattern on the print medium based on the adjusted
21 parameters.
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1 34. (Previously Presented) The printing apparatus of claim 33,
2 additionally comprising instructions for:

3 adjusting the number of the color patches to utilize the entire width of the
4 print media.

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6 35. (Previously Presented) The printing apparatus of claim 33, wherein
7 configuring the test pattern is performed after the width of the print media is
8 determined and before printing the test pattern is performed.
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11 36. (Previously Presented) The printing apparatus of claim 33, wherein
12 there is a lower limit to each of the height, the width and the number of the color
13 patch parameters.
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15 37. (Previously Presented) The printing apparatus of claim 33, wherein
16 configuring the test comprises instructions for:
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18 configuring differently sized print media to include different numbers of
19 color ramps per row, wherein for each size of print media, each row is filled
20 before another row is started, wherein height is minimized by including a plurality
21 of color ramps in a single row.
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1 38. (Previously Presented) The printing apparatus of claim 33, wherein
2 configuring the test pattern comprises instructions for:

3 locating a second color ramp relative to a first color ramp to minimize a
4 height of the test pattern, while maximizing a width of the test pattern.

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6 39. (Currently Amended) A printing apparatus configured for printing a
7 test pattern, comprising:

8 means for measuring a size of a print medium upon which the test pattern is
9 to be printed;

10 means for configuring the test pattern to include as many color ramps as
11 will fit per row based on the size of the print media and moving color ramps which
12 will not fit on a first row into a second row wherein the moved color ramp will fit
13 in its entirety, wherein the color ramps are arrayed horizontally along a width of
14 the print medium when the size of the print medium allows, and wherein the color
15 ramps are arrayed vertically when the size of the print medium require, and
16 wherein a size and number of color patches is adjusted according to space
17 available in completely fill a row in the test pattern; and
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19 means for printing the test pattern on the print medium.
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22 40. (Previously Presented) The printing apparatus of claim 39, wherein
23 the means for configuring the test pattern maximizes width and minimizes height
24 of a test pattern comprising a number of color ramps.
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2 41. (Previously Presented) The printing apparatus of claim 39, wherein
3 the means for configuring the test pattern adds a second row only when space does
4 not exist on a first row to add an additional element in the additional element's
5 entirety.

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7 42. (Previously Presented) The printing apparatus of claim 39, wherein
8 the means for configuring the test pattern configures differently sized print media
9 to include different numbers of elements per row, wherein for each size of print
10 media, each row is filed before another row is started.

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13 43. (Previously Presented) The printing apparatus of claim 39, wherein
14 the means for configuring the test pattern minimizes a height of the test pattern
15 and maximizes a width of the test pattern, wherein each element within the test
16 pattern comprises a color ramp fully contained on a row of the test pattern.

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19 44. (Previously Presented) The printing apparatus of claim 39, wherein:
20 the means for configuring the test pattern maximizes width of the test
21 pattern and minimizes height of the test pattern; and
22 the means for printing is adapted for printing on rolled media.
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